

3585 U.S. PTO
10/016143

12/17/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10016143	FILING DATE 12/17/2001	CLASS 257	SUBCLASS	SAU 2811	EXAMINER <i>[Signature]</i>
----------------------	---------------------------	--------------	----------	-------------	--------------------------------

****APPLICANTS:** Asano Tetsuro; Tsuchiya Hitoshi; Hirai Toshikazu;

****CONTINUING DATA VERIFIED:**

****FOREIGN APPLICATIONS VERIFIED:**

JAPAN 2001-121292 04/19/2001

JAPAN 2001-121293 04/19/2001

JAPAN 2001-141894 03/17/2001

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

492322002400

TITLE : Semiconductor switching device

U.S. DEPT. OF COMM./PAT & TM-PTO-4361 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)